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 $\geq$

A diagram of a 10-bit shift register. It consists of a horizontal row of 10 rectangular cells. The first cell on the left is labeled 'A' and the last cell on the right is labeled 'B'. Above the row of cells, there are two input lines: 'S' (Serial Input) and 'C' (Clock Input). The 'S' line is connected to the top of the first cell, and the 'C' line is connected to the top of the last cell. The cells are connected in a chain, with the output of one cell feeding into the input of the next cell.

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iphone5c
apple pay
speedmaster hb - sia
chrono24
iphone 6s
rt-dp11t/mk
iphone 5
sim
398
2018/7/19
chromehearts
s
(samantha thavasa)
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aknpy
n
disney() - buyma
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iphone6s plus 000 00.00 000 0000000 000 00000 00000000000 00(n00)0000chrome hearts(000000) 000 000000003370019880000000925000000000000000000
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iphone5c 000 00 iphone5s 000 000000 000 000000.0000000000no..